

# JARROD ELIASON

Senior Engineering Leader



## CONTACT

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## PROFILE

Engineering leader experienced in cross-functional project guidance and resource management with demonstrated competence directing the tasks of global teams. Results oriented and creative problem solver with broad exposure to technologies. High technical aptitude with ability to recognize the potential of existing and emerging technologies to create novel solutions.

## SKILLS

Project Scheduling (MS Project)  
Technical Data Analysis (MS Excel)  
Estimation and Proposal Generation  
Documentation (MS Word)  
Technical Presentation (MS Power Point)  
Electrical Design (Cadence OrCAD, Altium, Eagle)  
Root-cause Analysis (8D, 5 Why, Fishbone, Mindmap)  
Risk Management (FMEA)

## HOBBIES

First Lego League Coach and Volunteer  
Shipwreck Hunter  
Inventor  
Video and Music Producer

## WORK EXPERIENCE

### Logic PD, - Sr. Manager of Engineering Services

February 2015 – Present

As manager over Electrical and Mechanical Engineering, PCB Layout and Industrial Design, I have led multiple diverse cross-functional engineering projects for medical and industrial products. I manage local and remote engineering resources while performing design work and technical review. I am responsible for preparing detailed schedules and proposals and then executing projects to those schedules.

- System on module (SOM) based on multi-core application processor with LPDDR4 memory, eMMC, WiFi, Bluetooth, Ethernet (RGMII and Phy), SD Card, USB 3, USB 2, SPI, I2C, UART, SDIO, MIPI CSI-2 cameras and multiple display options: HDMI, LVDS, parallel RGB
- Automated dispensing machine utilizing machine vision and 3-axis stepper motor control
- Handheld RFID reader with integrated 2D barcode scanner based on SOM running Android and integrating multiple wireless technologies: UHF RFID, HF RFID, WiFi, GNSS (GPS)
- Precision linear position sensor using multiple 3-axis magnetic field sensors
- Prototype surgical navigation tool using off-the-shelf technology to provide early feedback from surgeons
- Breath acetone measurement device for home testing of fat-burning state

### Dancing LEDs, LLC - President and Founder

April 2014 – April 2015

I founded a company to develop a system of individually programmable Christmas lights controlled by a smart phone. The project was a technical success, but I exited the business after an unsuccessful crowdfunding campaign.

### SMART Modular Technologies – Sr. Manager

April 2013 – May 2014

Chief technical researcher for novel applications of memory and wireless technologies.

- Developed prototype PCB module and panel level test method using DRAM DIMM tester to achieve < 5 ppm temperature compensated crystal oscillator from standard crystals
- Researched wireless memory technologies and determined capabilities and power budget for a passive wireless gateway operable at less than one tenth the power of Bluetooth
- Our startup division closed when the company went up for sale

### Cypress Semiconductor / Ramtron - Product Design Manager

June 1998 - March 2013

I worked my way up from junior engineer to become manager and technical expert of ferroelectric nonvolatile memory (FRAM, F-RAM, FeRAM) at Ramtron, which was acquired by Cypress in 2012. As part of a small integrated circuit design company, I became fluent in all aspects of IC design including:

- Transistor level memory design
- SPICE simulation and Monte Carlo analysis
- Verilog based logic synthesis and verification
- Analog circuit design and verification

While at Ramtron, I spent 3-years on loan to Texas Instruments. I started as a technical contributor and by the third project I was leading a team of Ramtron and TI engineers on the design of the first 0.13um ferroelectric nonvolatile memory product. Later, at Ramtron, I led the redesign of 26 products with \$60 million annual revenue to be fabricated at TI. During my time at Ramtron and TI, I participated in technology research and development and was inventor or co-inventor on 18 patents.